



Number : GNO04
Subject: SDOS 4.0 & Memory Mapping
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In the past several of you have modified your slave cards to be able to map memory into common and prototype segments. The modification - which was made available but only to be implemented at your own risk and never officially supported - uses the signal SW on the slave card to switch the signal "user data".

User data is switched during all I/O actions in SLAVE MODE 1 and always on in SLAVE MODE 2. SW is the decoding of certain instruction types and although available within the hardware it was never implemented in the software until SDOS 4.0. Since the EXAM, FILL and DEBUG routines use the signal SW these commands will not execute correctly if used on a system with a modified slave card.

If memory mapping is required it should only be implemented by using the Hardware Analyser card which together with SDOS 4.0 allows every 128 byte segment of maximum 64K slave memory to be either internal or external (common or prototype).

Apart from the mapping facility the Hardware Analyser card also has a high speed memory to capture real time events in upto 256 bus transactions. For details see GNO03.

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