```
Number     :    HW005
Subject    :    2650A SLAVE CPU and
           :    a) Sense line during Trace
                b) Undesirable transitions of Flag line
Date       :    1980-08-21
```

a) During the tracing of a program in slave mode 1&2 or on the
   true status of the sense line is indicated but not correctly
   reacted upon by subsequent instructions if traced (SDOS 4.0).
   This problem can be resolved with the following modification
   although the internal sense line bus will now become inoperable.

   Remove   :   On slave PCB trace to IC B11 pin 6

   Add   . :   Jumper from IC B11 pin 6 to B11 pin 5

b) During Debug the state of the flag line can change at the TWICE
   cable. Inhibiting the flag line multiplexer from switching will
   cure this problem.

   Remove   :   Jumper 4 to 5 situated under IC E14

   Add   :   Jumper 5 to 6

NOTE: See HW001 for old style slave CPU